

## **Application Data Sheet**

### **Application Information**

Application number::

Filing Date::

Application Type:: Regular

Subject Matter:: Utility

Suggested classification::

Suggested Group Art Unit::

CD-ROM or CD-R?:: None

Number of CD disks::

Number of copies of CDs::

Sequence submission?::

Computer Readable Form (CRF)?::

Number of copies of CRF::

Title:: Synchronizing Circuit for Generating a Signal  
Synchronizing with a Clock Signal

Attorney Docket Number:: 001701.00129

Request for Early Publication?:: NO

Request for Non-Publication?:: NO

Suggested Drawing Figure::

Total Drawing Sheets:: 28

Small Entity?:: NO

Latin name::

Variety denomination name::

Petition included?:: NO

Petition Type::

Licensed US Govt. Agency::

Contract or Grant Numbers::

Secrecy Order in Parent Appl.?:: NO

10003313-120601  
T0903T-2FE00T

## Applicant Information

Applicant Authority Type:: Inventor  
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### Correspondence Information

Correspondence Customer Number:: 22907

### Representative Information

Representative Customer Number:: 22907

### Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Division of	09/505,204	02/16/00




### Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	11-038574	02/17/99	YES

### Assignee Information

Assignee name:: Kabushiki Kaisha Toshiba  
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